

# Configuration and Parameter Assignment Frame for the CPU 31x-2

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You **don't** need the information contained in this chapter if you configure and parameterize the CPU 31x-2 with *STEP 7* or with *COM PROFIBUS*.

You **don't** need the information contained in this chapter if you integrate the device master file of the CPU 31x-2 in your configuration tool, and then configure and parameterize the CPU 31x-2 using the configuration tool.

You **need** the information only in the following cases:

- If you don't use a configuration tool to create the configuration and parameter assignment frame.
- If you want to monitor the configuration and parameter assignment frame with a bus monitor.

## In This Chapter

In this chapter you will find all the information you need to configure and parameterize the address areas of the intermediate memory with a software tool.

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## 1.1 Structure of the Parameter Assignment Frame

### Definition: Parameter Assignment Frame

The parameter assignment frame contains all the parameterizable values of a DP slave. The parameter assignment frame has a maximum length of 244 bytes.

### Structure of the Parameter Assignment Frame

The length of the parameter assignment frame for the CPU 31x-2 is 10 bytes:

- Standard part (bytes 0 to 6)
- Parameters of the CPU 31x-2 (bytes 7 to 9)

### Standard Part

The first 7 bytes of the parameter assignment frame are standardized to EN 50170 and can, for example, have the following contents for the CPU 315-2:

<b>Byte 0</b>	88 <sub>H</sub>	Station status
<b>Byte 1</b>	01 <sub>H</sub>	WD factor 1
<b>Byte 2</b>	06 <sub>H</sub>	WD factor 2
<b>Byte 3</b>	0B <sub>H</sub>	minTsdr
<b>Byte 4</b>	80 <sub>H</sub>	Manufacturer ID, high byte;
<b>Byte 5</b>	2F <sub>H</sub>	Manufacturer ID, low byte
<b>Byte 6</b>	00 <sub>H</sub>	Group ID

Figure 1-1 Standard Part of the Parameter Assignment Frame (Example)

## Structure of the Parameters for the CPU 31x-2

The length of the parameters for the CPU 31x-2 is 3 bytes: The default setting of these 3 bytes is as follows: C0H 60H 00H.

The parameters have the following meaning:

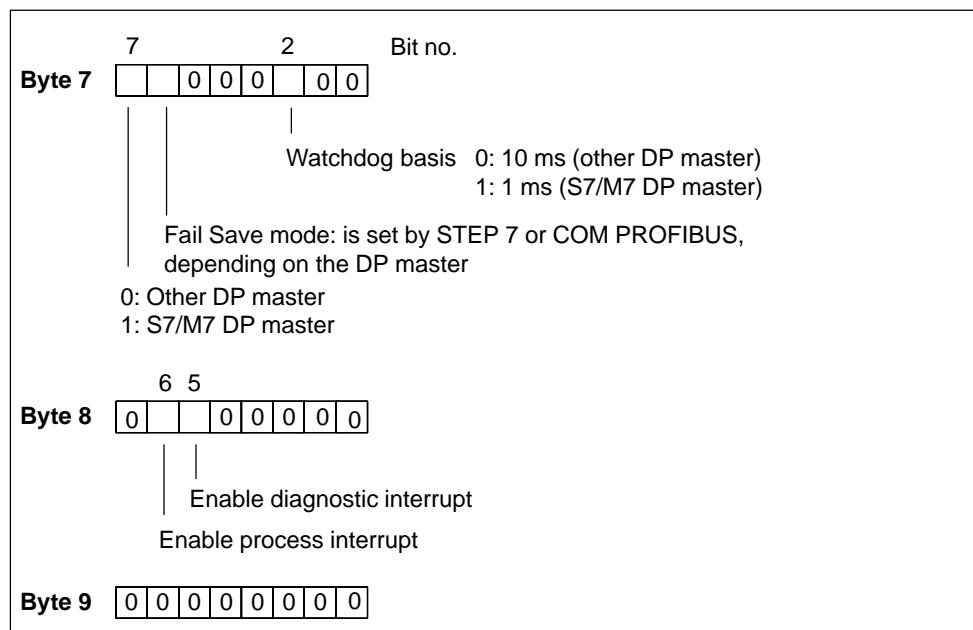


Figure 1-2 Parameters for the CPU 31x-2

## 1.2 Structure of the Configuration Frame (S7 Format)

### Structure of the Configuration Frame

The length of the configuration frame is dependent on the number of configured address areas of the intermediate memory of the CPU. The first 15 bytes of the configuration frame are permanently occupied. The structure of the configuration frame is as follows:

Table 1-1 Structure of the Configuration Frame

Configured Address Area	Byte				
	n	n + 1	n + 2	n + 3	n + 4
These bytes are permanently occupied:	04	00	00	AD	C4
	04	00	00	8B	41
	04	00	00	8F	C0
1st configured address area (n = 15)	See Table 1-2				
2nd configured address area (n = 20)					
...					
32nd configured address area (n = 170)					

## Identifiers for the Address Areas

The configuration identifiers are dependent on the type of address area. Table 1-2 shows all the identifiers for the address areas.

Table 1-2 Identifiers for the Address Areas of the Intermediate Memory

Address Area	Identifiers (Hexadecimal)				
	Special Identifier Format	Length Byte	Manufacturer-Specific Data (Comment length=3)		
	Byte 0	Byte 1	Byte 2	Byte 3	Byte 4
Input	See Figure 1-3	See Figure 1-4	00 <sub>H</sub>	83 <sub>H</sub>	40 <sub>H</sub>
Output			00 <sub>H</sub>	93 <sub>H</sub>	40 <sub>H</sub>

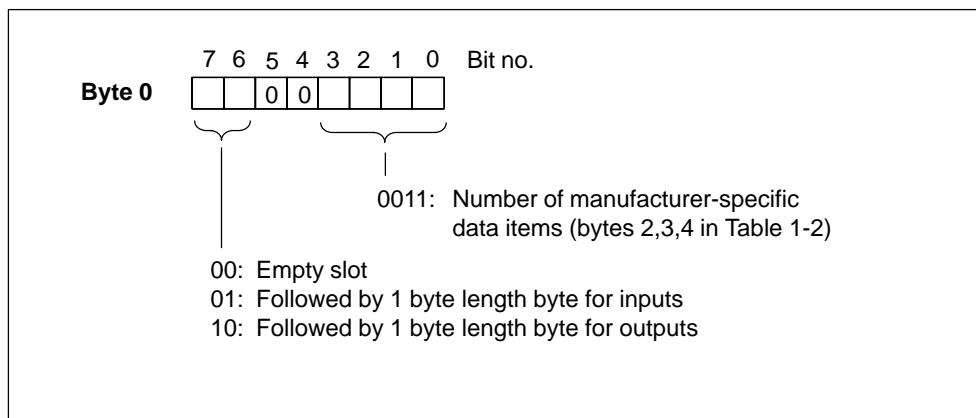


Figure 1-3 Meaning of the 0 Byte of the Address Area Identifiers of the CPU

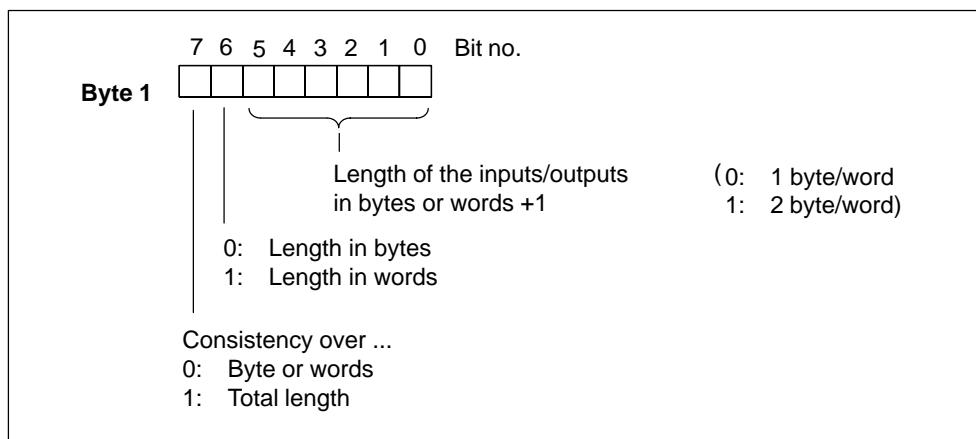


Figure 1-4 Meaning of Byte 1 of the Address Area Identifiers of the CPU

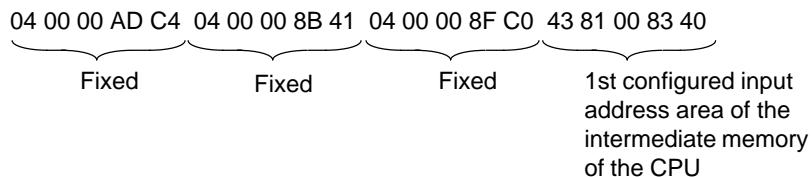
### Example of a Configuration Frame

Here is an example of a configuration frame for the CPU 315-2 DP:

Structure:

- A power supply
- The CPU 315-2 DP
- An input address area of the DP master (= output address area of the DP slave) with a length of 2 bytes and a consistency over the whole area

The configuration frame thus consists of 20 bytes and has the following structure:



## 1.3 Structure of the Configuration Frame for Non-S7 DP Masters

### Type/Device Master File

If your DP master does not support the configuration frame in S7 format (see Section 1.2), then you can obtain a type file/device master file in non-S7 format from the **SchnittStellenCenter** in Fürth.

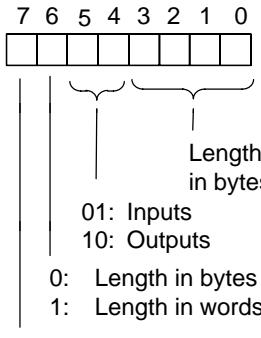
You can obtain the device master file via modem from the **SchnittStellenCenter** in Fürth under the telephone number 0911/737972.

## Structure of the Configuration Frame

The length of the configuration frame is dependent on the number of configured address areas of the intermediate memory of the CPU. The first 3 bytes of the configuration frame are permanently occupied with 0. The structure of the configuration frame is as follows:

You can only configure a maximum length of 16 bytes or 16 words in this format. At a length of 32 bytes, you therefore configure a length of 16 words.

Table 1-3 Structure of the Configuration Frame for Non-S7 DP Masters

Configured Address Areas	Byte
1st	0 0 0 0 0 0 0 0
2nd	0 0 0 0 0 0 0 0
3rd	0 0 0 0 0 0 0 0
4th	 <p>Bit no. In bytes or words Length of the inputs/outputs in bytes or words 01: Inputs 10: Outputs</p> <p>0: Length in bytes 1: Length in words</p> <p>Consistency across ... 0: Byte or word 1: Total length</p>
:	
:	
32nd	

